

1A,50-1000V Standard Rectifiers

Features

- Low leakage current
- Low forward voltage drop
- Glass passivated chip junction
- For general purpose applications
- Moisture sensitivity: level 1, per J-STD-020
- For fast switching and low logic level applications
- High temperature soldering guaranteed: 260°C/10 seconds



R-1

Applications

- Small battery charger, Power supplies

Maximum Ratings & Electrical Characteristics (T _A =25°C unless otherwise noted)									
Parameter	Symbol	1A1	1A2	1A3	1A4	1A5	1A6	1A7	Unit
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}	1							A
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load per diode	I _{FSM}	30							A
Operating junction temperature range	T _J	-55 to +135							°C
Storage temperature range	T _{STG}	-55 to +150							°C

Thermal-Mechanical Specifications (T _A =25°C unless otherwise noted)			
Parameter	Symbol	Typ	Unit
Thermal Resistance, Junction to Ambient	R _{θJA}	73	°C / W
Thermal Resistance, Junction to Case	R _{θJC}	36	°C / W
Thermal Resistance, Junction to Lead	R _{θJL}	25	°C / W

Electrical Specifications (T _A =25°C unless otherwise noted)										
Parameter	Symbol	Test Conditions	1A1	1A2	1A3	1A4	1A5	1A6	1A7	Unit
Forward Drop Voltage	V _F	I _F =1A				1.10				V
Reverse leakage current @V _R	I _R	T _J =25°C				0.5				uA
		T _J =125°C				100				
Typical junction capacitance	C _J	4.0 V 1 MHz				15				pF
Typical reverse recovery time	trr	I _F =0.5A, I _R =1.0A, I _{RR} =0.25A				1				uS

Note:

- Valid provided that leads at a distance of 9.5 mm from case are kept at ambient temperature.

Ratings and Characteristics Curves

($T_A = 25^\circ\text{C}$ unless otherwise noted)

FIG.1-FORWARD CURRENT DERATING CURVE

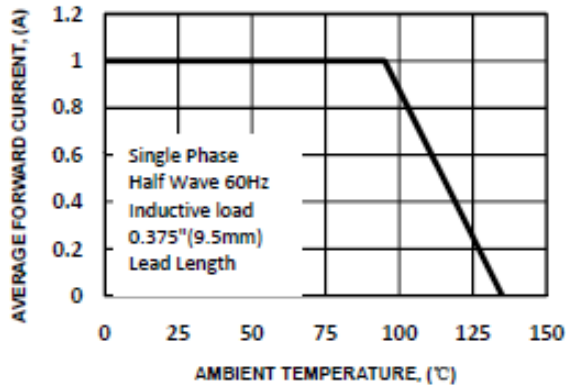


FIG 2-TYPICAL FORWARD CHARACTERISTICS

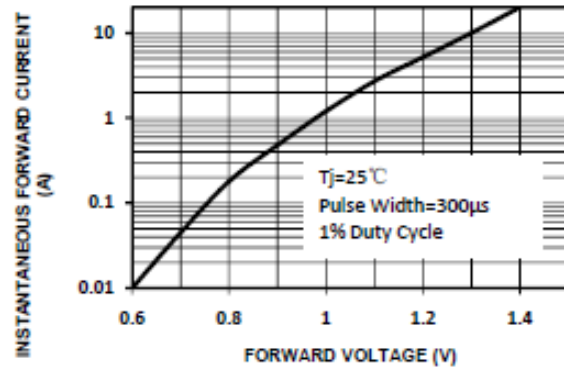


FIG 3-TYPICAL FORWARD CHARACTERISTICS

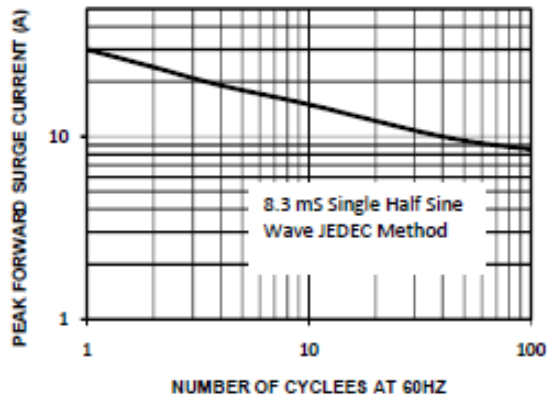


FIG 4-TYPICAL JUNCTION CAPACITANCE

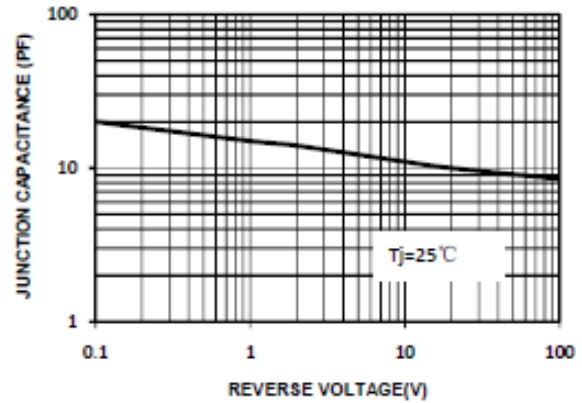
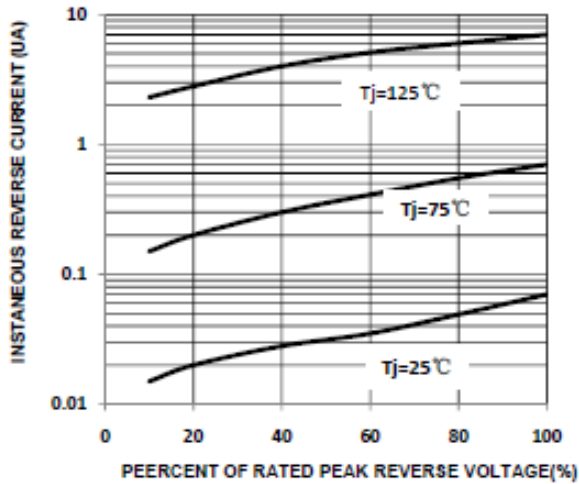
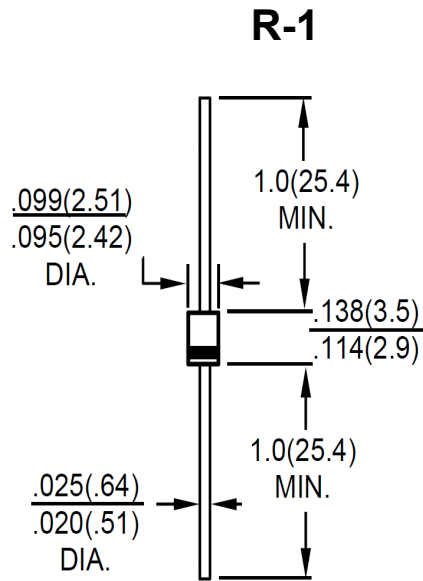


FIG5-TYPICAL REVERSE CHARACTERISTICS



Package Outline Dimensions

in inches (millimeters)



Dimensions in inches and (millimeters)

Revision History

Document Version	Date of release	Description of changes
Rev.A	2021.06.01	Released Datasheet
Rev.B	2024.02.22	Modify document format

Disclaimers

These materials are intended as a reference to assist our customers in the selection of the Suzhou Good-Ark product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Suzhou Good-Ark Electronics Co., Ltd. or a third party.

Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Suzhou Good-Ark Electronics Co., Ltd. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized Suzhou Good-Ark Electronics Co., Ltd. for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Suzhou Good-Ark Electronics Co., Ltd. by various means, including our website home page.

(<http://www.goodark.com>)

When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, Please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

The prior written approval of Suzhou Good-Ark Electronics Co., Ltd. is necessary to reprint or reproduce in whole or in part these materials.

Please contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized distributor for further details on these materials or the products contained herein.